

Register Machine computation works with natural numbers and the associated elementary operations of increment/decrement/zero-test. It abstracts away from any particular, concrete representation of numbers (e.g. as bit strings). Turing's original model of computation (now called a Turing machine) is more concrete. Numbers are represented in terms of a fixed, finite alphabet of symbols and the increment/decrement/zero-test programmed in terms of more elementary symbol-manipulating operations. In fact, Turing argued that he had formalized the notion of "algorithm" in the most concrete possible form.



A Turing machine consists of a linear tape unbounded to the left and right, which is divided into cells. Each cell contains either a symbol from a finite alphabet of *tape symbols*, in this case 0 and 1, or the special blank symbol \Box . Only finitely many cells may contain non-blank symbols. In slide 3, the Turing machine is in state q with its tape head (the arrow) scanning the tape cell with tape symbol 0.



There are many variations of the definition of a Turing machine. The definition of a Turing machine in this course consists of a two-way-infinite tape which starts at the left of the input string. It is also possible to define Turing machines where the tape is infinite in only one direction, or that can leave the tape head stationary as well as moving left or right. These variants all have the same computational power.



The machine has finite internal memory. It can remember which state it is in, nothing more. At each step of the computation, the machine reads the symbol currently under the tape head. If the machine is currently in state q and reading symbol a then $\delta(q, a)$ tells the machine what to do next. If $\delta(q, a)$ is undefined, the machine simply halts. Otherwise, $\delta(q, a) = (q', a', d)$ for some state q', symbol a' and direction d. The machine overwrites the current cell with the symbol a', moves along the tape in direction d (L for left, R for right), and changes its state to q'.

Now we need to describe formally what it means to compute with a Turing machine. In an analogous fashion to register-machine configurations, we define the notion of Turing-machine configurations.



 $\begin{array}{l} \textbf{first and last} \\ \textbf{Define the functions first} : \Sigma^* \to \Sigma \times \Sigma^* \text{ and} \\ \textbf{last} : \Sigma^* \to \Sigma \times \Sigma^* \text{ as follows} \\ \textbf{first}(w) = \begin{cases} (a,v) & \text{if } w = av \\ (..,\epsilon) & \text{if } w = \epsilon \end{cases} \\ \textbf{last}(w) = \begin{cases} (a,v) & \text{if } w = va \\ (..,\epsilon) & \text{if } w = \epsilon \end{cases} \end{array}$

These functions split off the first and last symbols of a string, splitting off $_$ if the string is empty.

Slide 6



Turing Machine Computation A computation of a TM *M* is a (finite or infinite) sequence of configurations $c_0, c_1, c_2, ...$ where • $c_0 = (s, \epsilon, u)$ is an initial configuration • $c_i \rightarrow_M c_{i+1}$ holds for each i = 0, 1, ...The computation • does not halt if the sequence is infinite • halts if the sequence is finite and its last element (q, w, u) is a normal form.

Slide 7



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Example Computation
$$(s, \epsilon, \Box^{n}0) \rightarrow_M (q, \Box, \Box^n 0)$$
 $\rightarrow_M (q, \Box^1, 1^{n-1}0)$ \vdots $\rightarrow_M (q, \Box^{n}, 0)$ $\rightarrow_M (q, \Box^{n+1}, \varepsilon)$ $\rightarrow_M (q', \Box^{n+1}, 10)$ $\rightarrow_M (q', \Box^{n-1}, 110)$ \vdots $\rightarrow_M (q', \Box, 1^{n+1}0)$











The behaviour of the RM gadget $(Q, A, D) ::= \delta(Q, A)$ has already been described. The behaviour of RM gadget $\lceil [Q, W, U] \rceil ::= C$ is to decode the value of register C to obtain values for the registers Q, Wand U. The behaviour of RM gadget $C ::= \lceil [Q, W, U] \rceil$ is to code the list of values of registers Q, W and U to obtain a new value for C. The behaviour of the RM gadget Q < 2 is evident.

We've seen that a Turing machine's computation can be implemented by a register machine. The converse holds: the computation of a register machine can be implemented by a Turing machine. To make sense of this, we first have to fix a tape representation of RM configurations, and hence of numbers, lists of numbers.... We will not give the full implementation. We will demonstrate how to encode lists of numbers, and use this enoding to describe **Turing computable** functions.





Theorem. A partial function is Turing computable if and only if it is register machine computable.

Proof (sketch). We've seen how to implement any TM by a RM. Hence

f TM computable implies f RM computable.

For the converse, one has to implement the computation of a RM in terms of a TM operating on a tape coding RM configurations. To do this, one has to show how to carry out the action of each type of RM instruction on the tape. It should be reasonably clear that this is possible in principle, even if the details are omitted (because they are tedious).

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Notions of computability

- Church (1936): λ-calculus
- Turing (1936): **Turing machines**.

Turing showed that the two very different approaches determine the same class of computable functions. Hence:

Church-Turing Thesis. Every algorithm [in intuitive sense] can be realized as a Turing machine.



The work on Turing machines provided one path to the invention of computers, and plays a significant role in **complexity theory**, the classification of computational problems according to their inherent difficulty and relating these classes to each other. The work on partial recursive functions has led to a branch of mathematics called **recursion theory**. The work on the λ -calculus has led to a branch of computer science called **functional programming**.